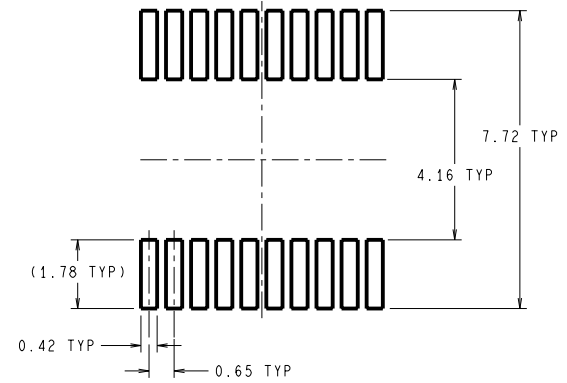
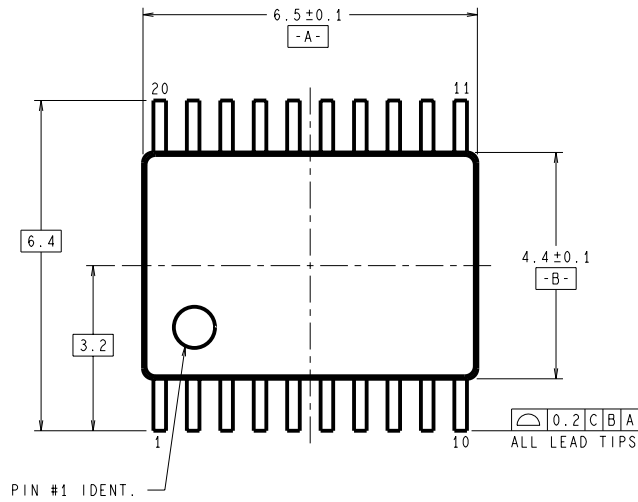
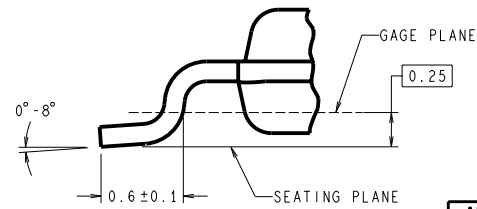
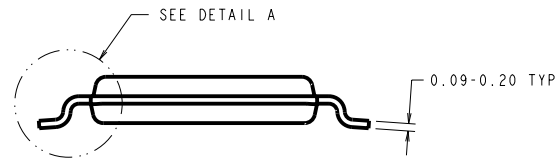
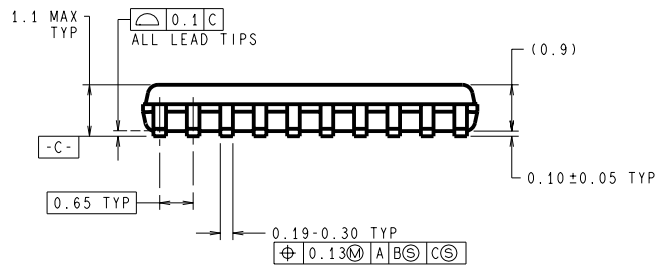


REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
D	REVISE & REDRAW ON PRO/E PER CURRENT STD; CORRECT DET CALLOUT FROM D TO A.	11105	08/29/95	MS/



LAND PATTERN RECOMENDATION



DETAIL A  
TYPICAL, SCALE: 40X

NOTES: UNLESS OTHERWISE SPECIFIED

1. REFERENCE JEDEC REGISTRATION MO-153, VARIATION AC, DATED 7/93.

APPROVALS		DATE	National Semiconductor		
DRAWN	MARTA SUCHY	08/29/95	2900 Semiconductor dr., Santa Clara, CA 95052-8090		
DFTG. CHK.			MOLDED TSSOP, JEDEC, 4.4mm BODY WIDTH, 20 LD, 0.65mm PITCH		
ENGR. CHK.					
PROJECTION		SCALE	SIZE	DRAWING NUMBER	REV
INCH [MM]		N/A	C	MKT-MTC20	D
DO NOT SCALE DRAWING			SHEET 1 of 1		